

Notice of References Cited	Application/Control No. 09/590,332	Applicant(s)/Patent Under Reexamination COWLEY ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,607,393	08-1986	Nolde et al.	455/208
	B	US-6,370,360	04-2002	Kunkel, Lars Peter	455/76
	C	US-5,896,061	04-1999	Behrent, Hermann	329/308
	D	US-6,188,291	02-2001	Gopinathan et al.	331/45
	E	US-5,789,987	08-1998	Mittel et al.	331/17
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP0253680	07-1987	GB,SE,FR,DE	Isao et al.	
	O	GB2319913	03-1998	GB	Watanabe, Nozomu	
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shahani et al., A 115mW CMOS GPS Receiver, Dec.1998, ISSCC98 paper Ref. FA 8.1
*	V	Ulrich L. Rohde, Digital PLL Frequency Synthesizers, 1983, Prentice Hall, ISBN 013 214 293-2
	W	Floyd M. Gardner, Phaselock Techniques, Second Edition, 1979, Chapter 10, Pages 208-209.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.